4)

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In the Title:

METHODS OF READING AND/OR WRITING DATA TO MEMORY DEVICES INCLUDING VIRTUAL GROUND LINES AND/OR MULTIPLE WRITE CIRCUITS <u>AND/OR VIRTUAL GROUND LINES</u> AND RELATED DEVICES

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In the Specification:

Please amend the Related Application section at page 1, lines 6-12 of the specification as indicated below:

RELATED APPLICATION

This application also claims the benefit as a divisional of U.S. Patent Application Serial No. 10/123,601, filed April 16, 2002, now U.S. Patent No. 6,674,670, issued on January 6, 2004, the disclosure of which is hereby incorporated herein by reference in its entirety as if set forth fully herein. This application also claims the benefit of Korean Patent Application No. 2001-24685, filed May 7, 2001, the disclosure of which is hereby incorporated herein by reference in its entirety as if set forth fully herein.